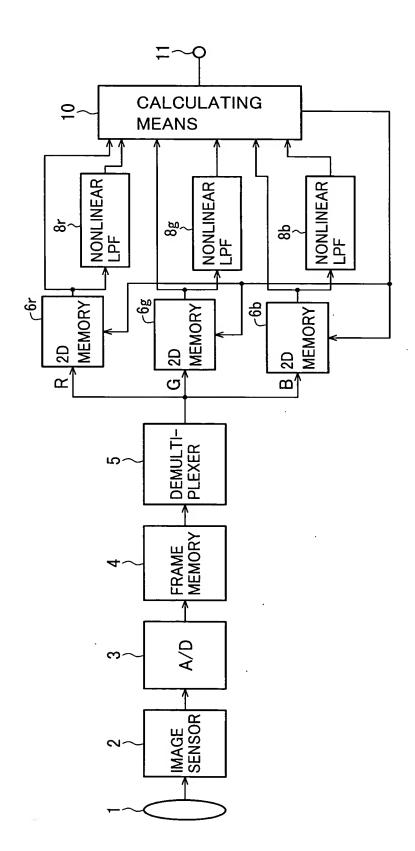
App No.: NEW Docket No.: 1190-0629P Inventor: Tetsuya KUNO et al.
Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL PROCESSING METHOD NEW SHEET Sheet 1 of Sh Docket No.: 1190-0629PUS1

Sheet 1 of 21



Docket No.: 1190-0629PUS1

App No.: NEW Docket No.: 1190-0629P Inventor: Tetsuya KUNO et al.
Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL PROCESSING METHOD

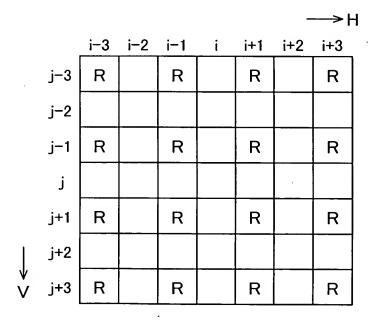
NEW SHEET

Sheet 2 of 21

FIG.2

						-	> ⊦
	i-3	i−2	i-1	i	i+1	i+2	i+3
j - 3	R	G	R	G _,	R	G	R
j-2	G	В	G	В	G	В	G
j-1	R	G	R	G	R	G	R
j	G	В	G	В	G	В	G
j+1	R	G	R	G	R	G	R
j+2	G	В	G	В	G	В	G
j+3	R	G	R	G	R	G	R
	j−2 j−1 j j+1 j+2	 j−3 R j−2 G j−1 R j G j+1 R j+2 G 	 j−3 R G j−2 G B j−1 R G B j+1 R G j+2 G B 	j-3 R G R j-2 G B G j-1 R G R j G B G j+1 R G R j+2 G B G	j-3 R G R G j-2 G B G B j-1 R G R G j G B G B j+1 R G R G j+2 G B G B	j-3 R G R G R j-2 G B G B G j-1 R G R G R j G B G R j+1 R G R G R j+2 G B G B G	j-3 R G R G R G G G G G G G G G G G G G G

FIG.3



Docket No.: 1190-0629PUS1 App No.: NEW

Inventor: Tetsuya KUNO et al.

Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL

PROCESSING METHOD **NEW SHEET**

Sheet 3 of 21

FIG.4

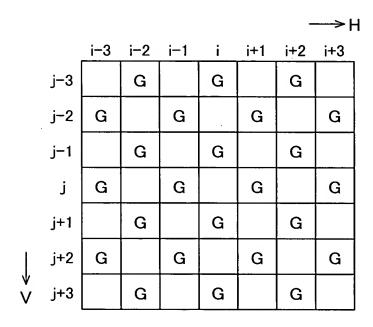
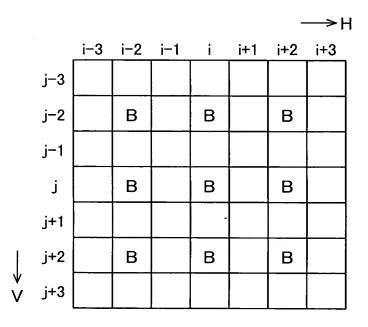


FIG.5



App No.: NEW Docket No.: 1190-0629Pi Inventor: Tetsuya KUNO et al. Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL PROCESSING METHOD Docket No.: 1190-0629PUS1

NEW SHEET

Sheet 4 of 21

4/21

FIG.6

	i−3	i−2	i−1	i	i+1	i+2	i+3
ј−3	RNF						
j−2	RNF						
j−1	RNF						
j	RNF						
j+1	RNF						
j +2	RNF						
j+3	RNF						

FIG.7

	i−3	i-2	i-1	i	i+1	i+2	i+3
j − 3	GNF						
j−2	GNF						
j−1	GNF						
j	GNF						
j+1	GNF						
j+2	GNF						
j + 3	GNF						

Docket No.: 1190-0629PUS1 Inventor: Tetsuya KUNO et al.

Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL PROCESSING METHOD

NEW SHEET

Docket No.: 1190-0629P

Docket No.: 1190-0629P

Docket No.: 1190-0629P

NEV SHEET

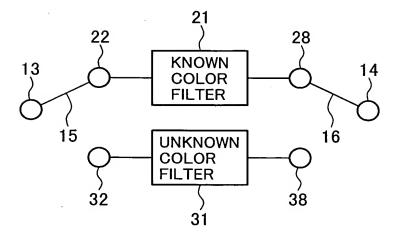
Sheet 5 of

Sheet 5 of 21

FIG.8

	i-3	i-2	i-1	i	i+1	i+2	i+3
j−3	BNF						
j-2	BNF						
j-1	BNF						
j	BNF						
j+1	BNF						
j+2	BNF						
j+3	BNF						

FIG.9



Docket No.: 1190-0629PUS1

Inventor: Tetsuya KUNO et al.

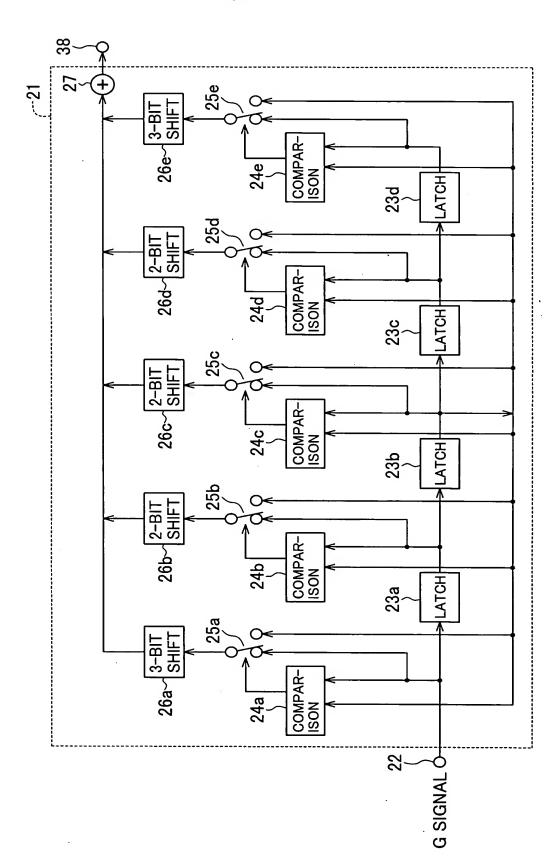
Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL

PROCESSING METHOD

NEW SHEET

Sheet 6 of 21

6/21

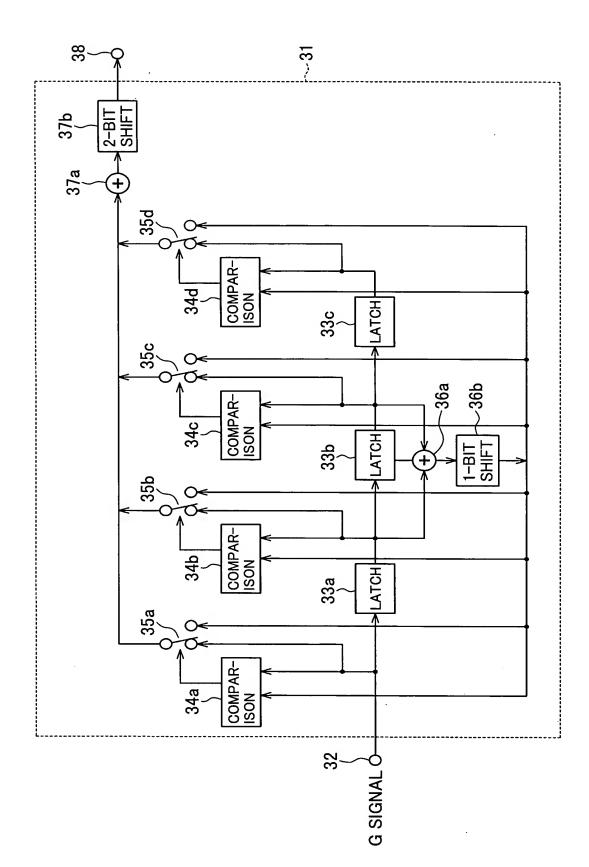


Docket No.: 1190-0629PUS1

App No.: NEW Docket No.: 1190-0629P
Inventor: Tetsuya KUNO et al.
Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL
PROCESSING METHOD
NEW SHEET Sheet 7 of

Sheet 7 of 21

7/21



App No.: NEW Docket No.: 1190-0629P Inventor: Tetsuya KUNO et al.
Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL PROCESSING METHOD Docket No.: 1190-0629PUS1

NEW SHEET

Sheet 8 of 21

FIG.12

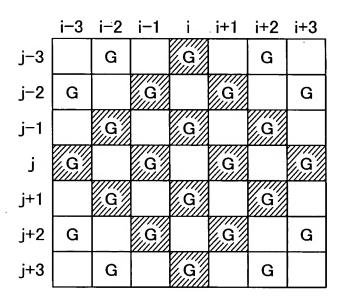


FIG.13

	i−5	i-4	i-3	i-2	i-1	_ i	i+1	i+2	i+3
j−4	G		G				G		G
j-3		G				G		G	
j-2	G		G		G				G
j - 1						G			
j	G		G		G				G
j+1				G		G			
j+2	G		G						G
j+3		G				G	,	G	
j+4	G		G				G		G

App No.: NEW Docket No.: 1190-0629P Inventor: Tetsuya KUNO et al.
Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL PROCESSING METHOD NEW SHEET Sheet 9 co. Docket No.: 1190-0629PUS1

Sheet 9 of 21

FIG.14

i	i-3	i-2	i-1	i	i+1	i+2	i+3	i+4
j-3	R		R		R		R	
j−2								
j−1	R		R		R		R	
j								
j+1	R		R		R		R	
j+2					,			
j+3	R		R		R		R	
j+4								

FIG.15

	i-4	i−3	i-2	i−1	. i	i+1	i+2	i+3
j-3		R		R		R		R
j-2								
j−1		R		R		R		R
j								
j+1		R		R		R		R
j+2								,
j+3		R		R		R		R
j+4								

Inventor: Tetsuya KUNO et al.

Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL PROCESSING METHOD

NEW SHEET

Sheet 10 of 21

Docket No.: 1190-0629PUS1

FIG.16

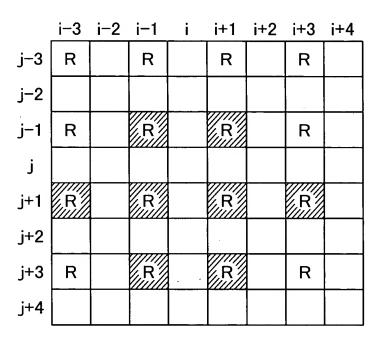


FIG.17

ı	i-4	i−3	i-2	i-1	i	i+1	i+2	i+3
j-3		R	,	R		R		R
j−2								
j−1		R		R		R		R
j								
j+1		R		R		R		R
j+2								
j + 3		R		R		R		R
j + 4								

Docket No.: 1190-0629PUS1

Inventor: Tetsuya KUNO et al.

Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL PROCESSING METHOD

NEW SHEET

Sheet 11 of 21

FIG.18

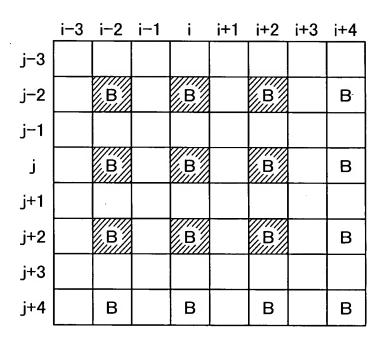


FIG.19

	i-4	i-3	i-2	i-1	i	i+1	i+2	i+3
j-3								
j-2	В		В		B		В	•
j - 1								
j	B		B		B		B	
j+1								
j+2	В		В		В		В	
j+3 j+4								
j+4	В		В		В		В	

Docket No.: 1190-0629PUS1 Inventor: Tetsuya KUNO et al.

Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL PROCESSING METHOD

NEW SHEET

Docket No.: 1190-0629P

Docket No.: 1190-0629P

SIGNAL PROCESSING METHOD

Sheet 12 of the process of the process

Sheet 12 of 21

FIG.20

	i-3	i-2	i-1	i	i+1	i+2	i+3	i+4
j-3				·				•
j−2		В		В		В		В
j -1						•		
j		B		B		B		В
j+1								
j+2		B		B		B		В
j+2 j+3 j+4								
j+4		В		В		В	,	В

FIG.21

·	i-4	i−3	i-2	i-1	i	i+1	i+2	i+3
j-3								
j-2	В		В		В		В	
j-1								·
j	В		B		B		В	
j+1								
j+2	В		B		B		В	
j+3								
j+4	В		В		В		В	

App No.: NEW Docket No.: 1190-0629PUS1

Inventor: Tetsuya KUNO et al.

Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL

PROCESSING METHOD

NEW SHEET Sheet 13 of 21

FIG.22

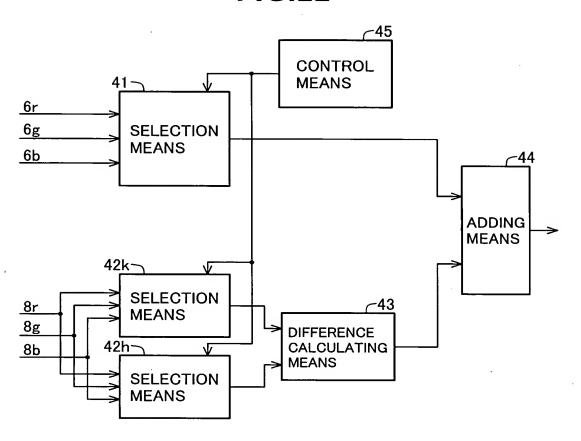
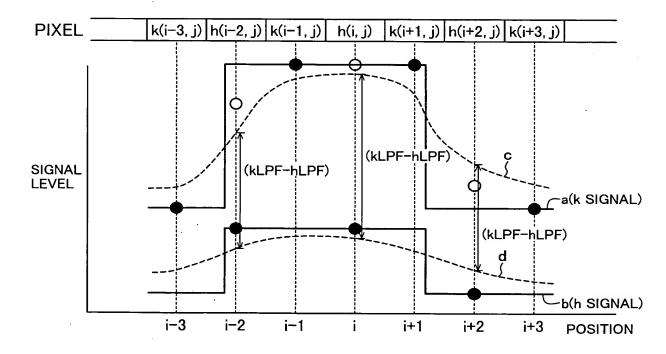


FIG.23



App No.: NEW Docket No.: 1190-0629PUS1 Inventor: Tetsuya KUNO et al.

Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL

PROCESSING METHOD NEW SHEET

Sheet 14 of 21

FIG.24

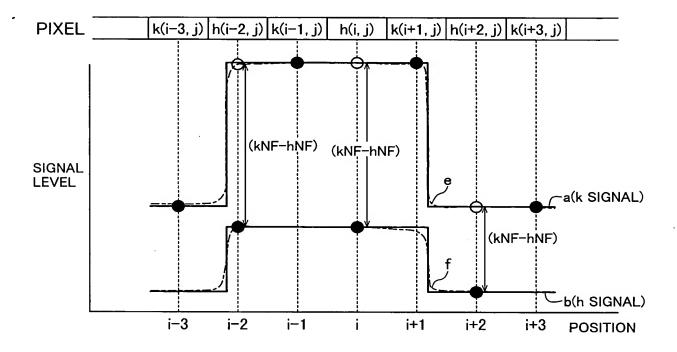
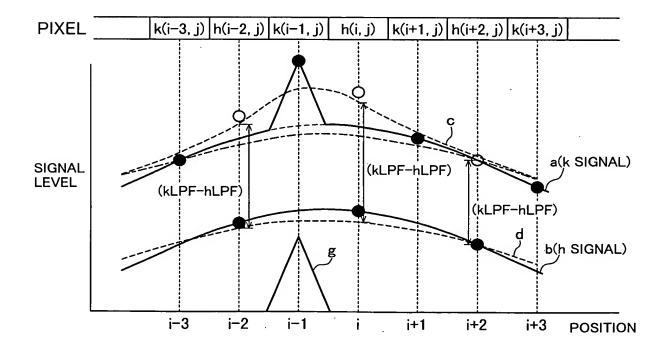


FIG.25



Docket No.: 1190-0629PUS1

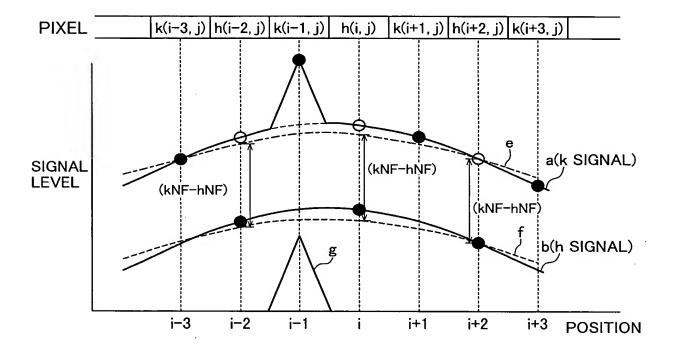
Inventor: Tetsuya KUNO et al.

Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL PROCESSING METHOD

NEW SHEET

Sheet 15 of 21

FIG.26



Docket No.: 1190-0629PUS1 Inventor: Tetsuya KUNO et al.

Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL PROCESSING METHOD

NEW SHEET

Sheet 16 of 21

FIG.27

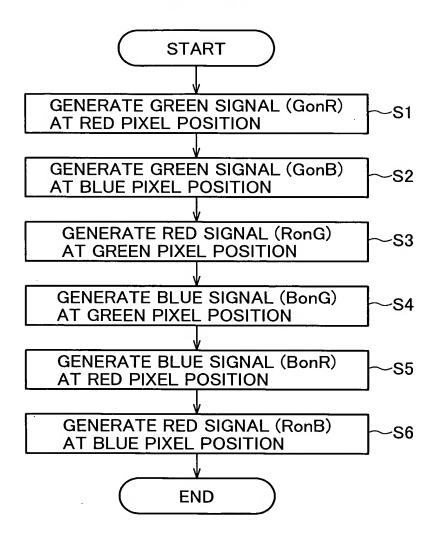


FIG.28

	i-3	i−2	i-1	i	<u>i+1</u>	i+2	i+3
j−3	gr	G	gr	G	gr	G	gr
j−2	G		G		G		G
j−1	gr	G	gr	G	gr	G	gr
j	G		G		G		G
j+1	gr	G	gr	G	gr	G	gr
j+2	G		G		G		G
j+3	gr	G	gr	G	gr	G	gr

Docket No.: 1190-0629PUS1

App No.: NEW Docket No.: 1190-0629P Inventor: Tetsuya KUNO et al.
Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL PROCESSING METHOD

NEW SHEET

Sheet 17 of 21

17/21

FIG.29

	i-3	i−2	i-1	i	i+1	i+2	i+3
j−3	gr	G	gr	G	gr	G	gr
j-2	G	gb	G	gb	G	gb	G
j−1	gr	G	gr	G	gr	G	gr
j	G	gb	G	gb	G	gb	G
j+1	gr	G	gr	G	gr	G	gr
j+2	G	gb	G	gb	G	gb	G
j+3	gr	G	gr	G	gr	G	gr

FIG.30

	i-3	i−2	i-1	i	i+1	i+2	i+3
j−3	R	rg	R	rg	R	rg	R
j−2	rg		rg		rg		rg
j−1	R	rg	R	rg	R	rg	R
j	rg		rg		rg		rg
j+1	R	rg	R	rg	R	rg	R
j + 2	rg		rg		rg		rg
j+3	R	rg	R	rg	R	rg	R

FIG.31

	i−3	i-2	<u>i-1</u>	i	j+1	i+2	i+3
j−3		bg		bg		bg	
j-2	bg	В	bg	В	bg	В	bg
j−1		bg		bg		bg	
j	bg	В	bg	В	bg	В	bg
j+1		bg		bg		bg	
j+2	bg	В	bg	В	bg	В	bg
j+3		bg		bg		bg	

Inventor: Tetsuya KUNO et al.
Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL

PROCESSING METHOD

NEW SHEET

Sheet 18 of 21

Docket No.: 1190-0629PUS1

18/21

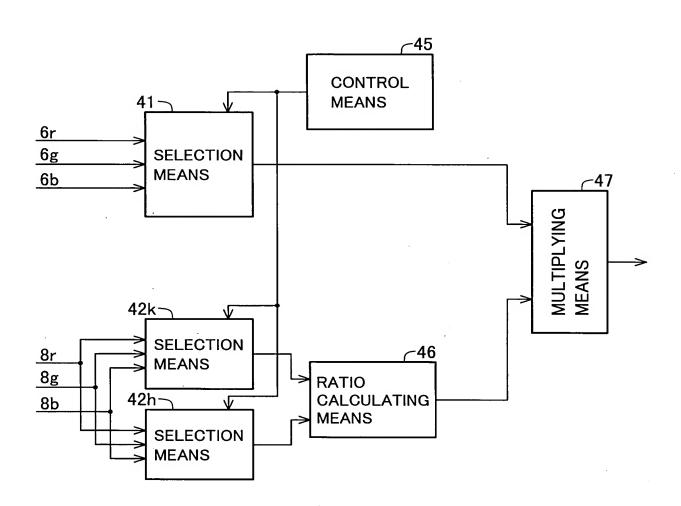
FIG.32

FIG.33

	i−3	i-2	i−1	i	<u>i+1</u>	i+2	i+3
j−3	br	bg	br	bg	br	bg	br
j−2	bg	В	bg	В	bg	В	bg
j−1	br	bg	br	bg	br	bg	br
j	bg	В	bg	В	bg	В	bg
j+1	br	bg	br	bg	br	bg	br
j +2	bg	В	bg	В	bg	В	bg
j + 3	br	bg	br	bg	br	bg	br

App No.: NEW
Inventor: Tetsuya KUNO et al.
Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL
PROCESSING METHOD
NEW SHEET
Sheet 19 of 21

FIG.34



Inventor: Tetsuya KUNO et al.

Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL PROCESSING METHOD

NEW SHEET

Sheet 20 of 21

Docket No.: 1190-0629PUS1

FIG.35

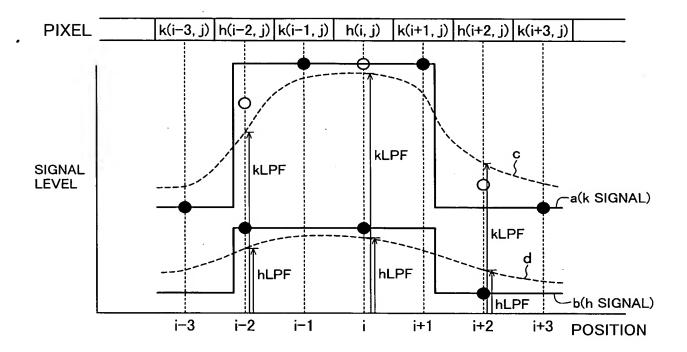
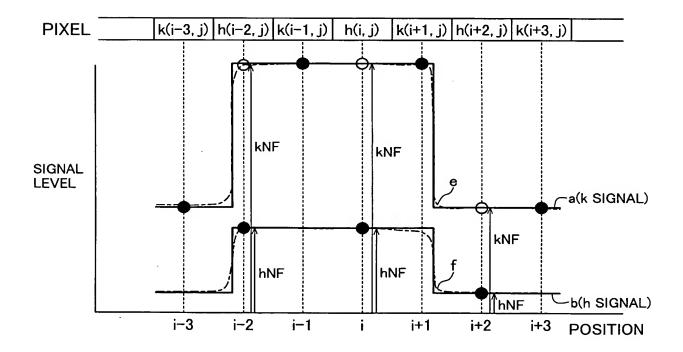


FIG.36



Inventor: Tetsuya KUNO et al.
Title: PIXEL SIGNAL PROCESSING APPARATUS AND PIXEL SIGNAL

Docket No.: 1190-0629PUS1

PROCESSING METHOD

NEW SHEET

Sheet 21 of 21

FIG.37

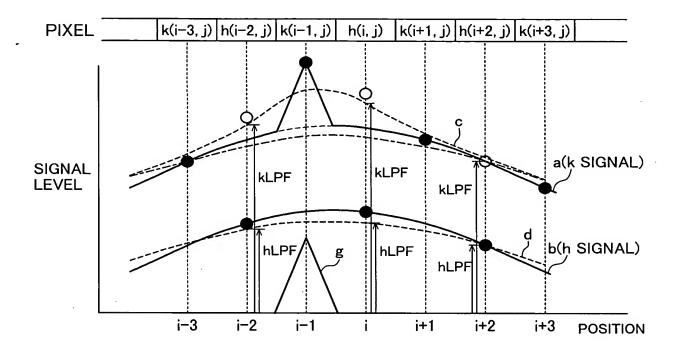


FIG.38

